DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Electronic Version v11
Stylesheet Version v10

Title of Invention

CURRENT PATH BETWEEN PRINTED CIRCUIT BOARDS FOR ELECTROSTATIC DISCHARGE PROTECTION

As the below named inventors, we declare that:

This declaration is directed to the invention titled: "CURRENT PATH BETWEEN PRINTED CIRCUIT BOARDS FOR ELECTROSTATIC DISCHARGE PROTECTION"

We believe that we are the original and first inventors of the subject matter which is claimed and for which a patent is sought;

We have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

We acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTORS:

Inventor 1: Tung-Wei KAO	Inventor
Signature :	Citizen of : TW
Inventor 2: Hung-I LAI	Inventor
Signature :	Citizen of : TW
Inventor 3: Chia-Hung YEN	Inventor

Signature : Citizen of : TW	

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

I believe I am the sole (if only one name appears below), or joint (if more than one name appears), original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

"CURRENT PATH BETWEEN PRINTED CIRCUIT BOARDS FOR ELECTROSTATIC DISCHARGE PROTECTION"

+	The specification for the	above entitled invention is filed herewith.
	The specification for the	above entitled invention was filed previously
with appl	ication serial number:	Filing Date:
spe		eviewed and understand the contents of the above identified as amended by any amendment referred to above.
Regu appli to th becan	nvention disclosed in this ap- lations, Section 1.56 (a). I for cation to disclose to the Patent se e patentability of the invention	sclose information which is material to the patentability of plication in accordance with Title 37, Code of Federal arther acknowledge the duty in any continuation-in-part and Trademark Office all information known to be material a disclosed in this application, as defined in 1.56, which filling date of the prior application and the filling date of this
]	PRIORITY CLAIM
	There is no claim of priorit	y
+,0	Claim of priority is based of No. The Taiw	in the following: an, R.O.C.: 092206500 iwan, R.O.C.: 2003/4/22
T ₁	ling date in Ta	iwan, R.O.C.: 2007/4/22
	•	ER OF ATTORNEY
		hereby appoint the following attorneies to prosecute this of business in the Patent and Trademark Office: Registration# 41,526
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DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

(請發明人務必簽署日期)

Date: > 0044, 8

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